

Integrated Circuit

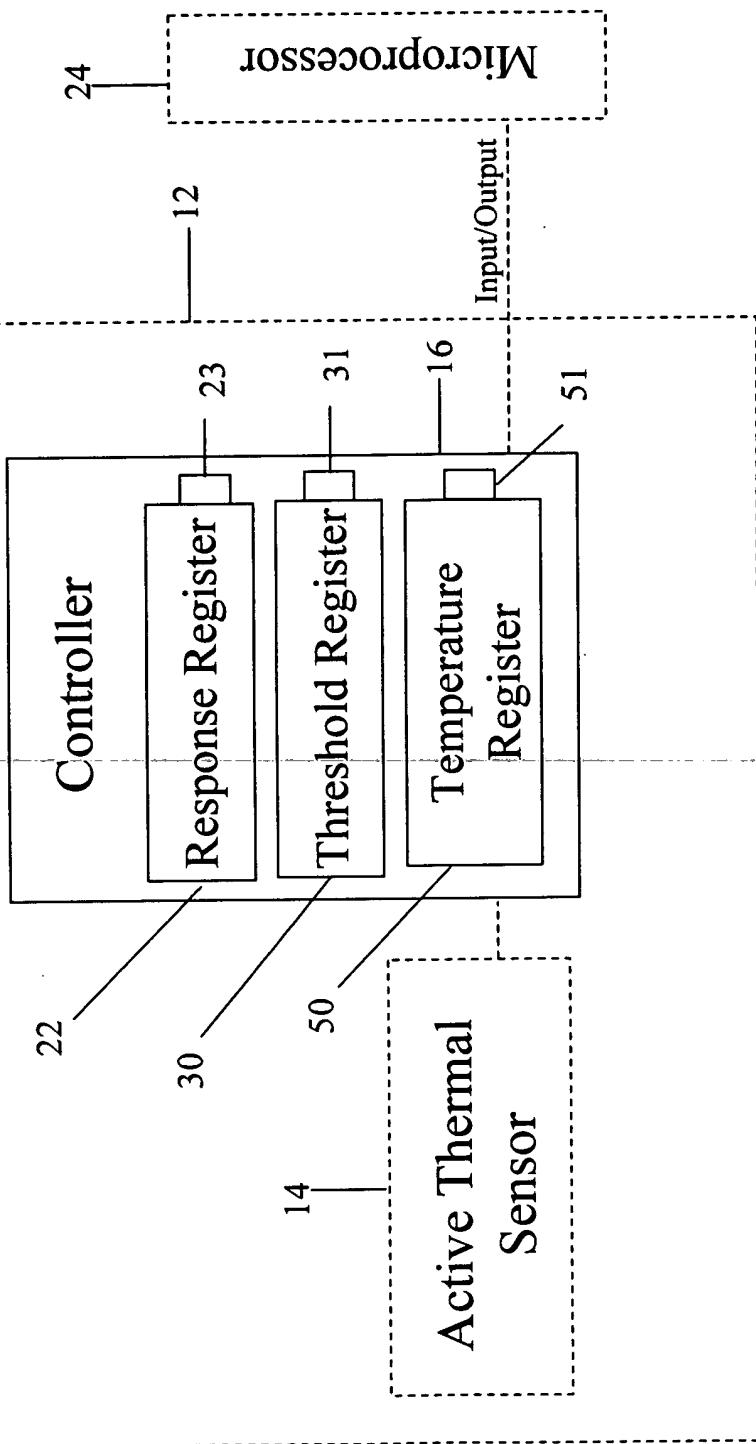


Figure 1A

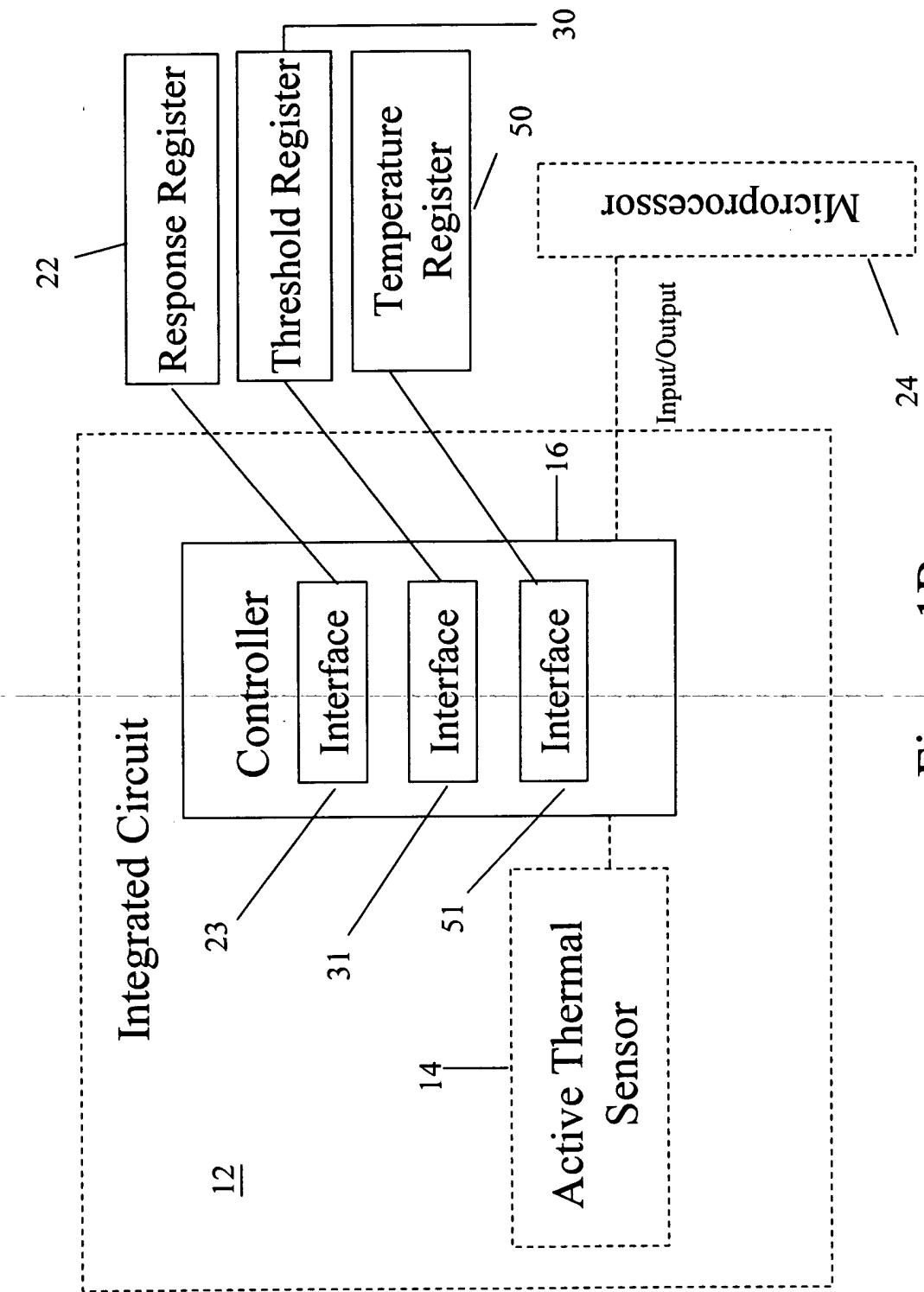


Figure 1B

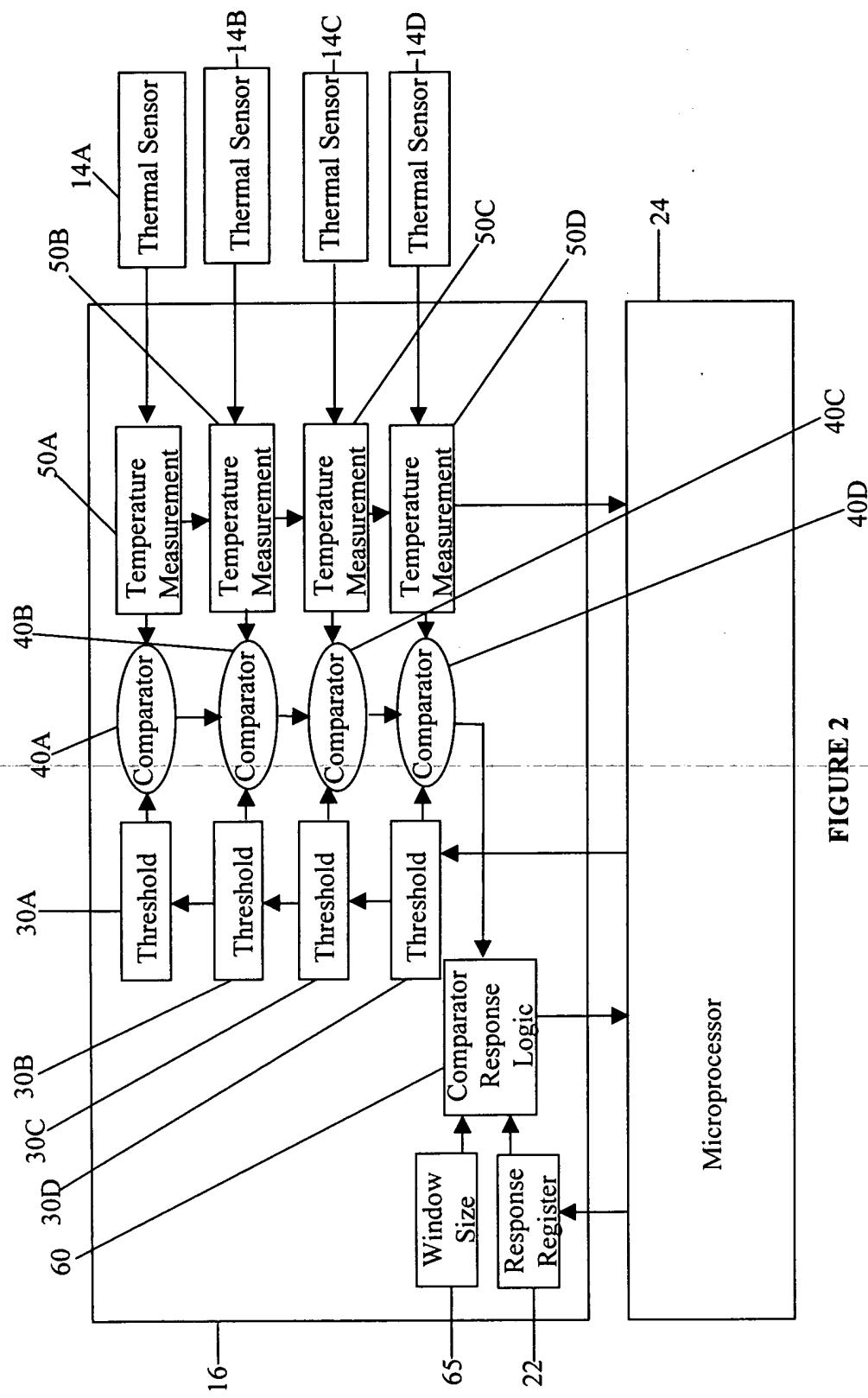


FIGURE 2

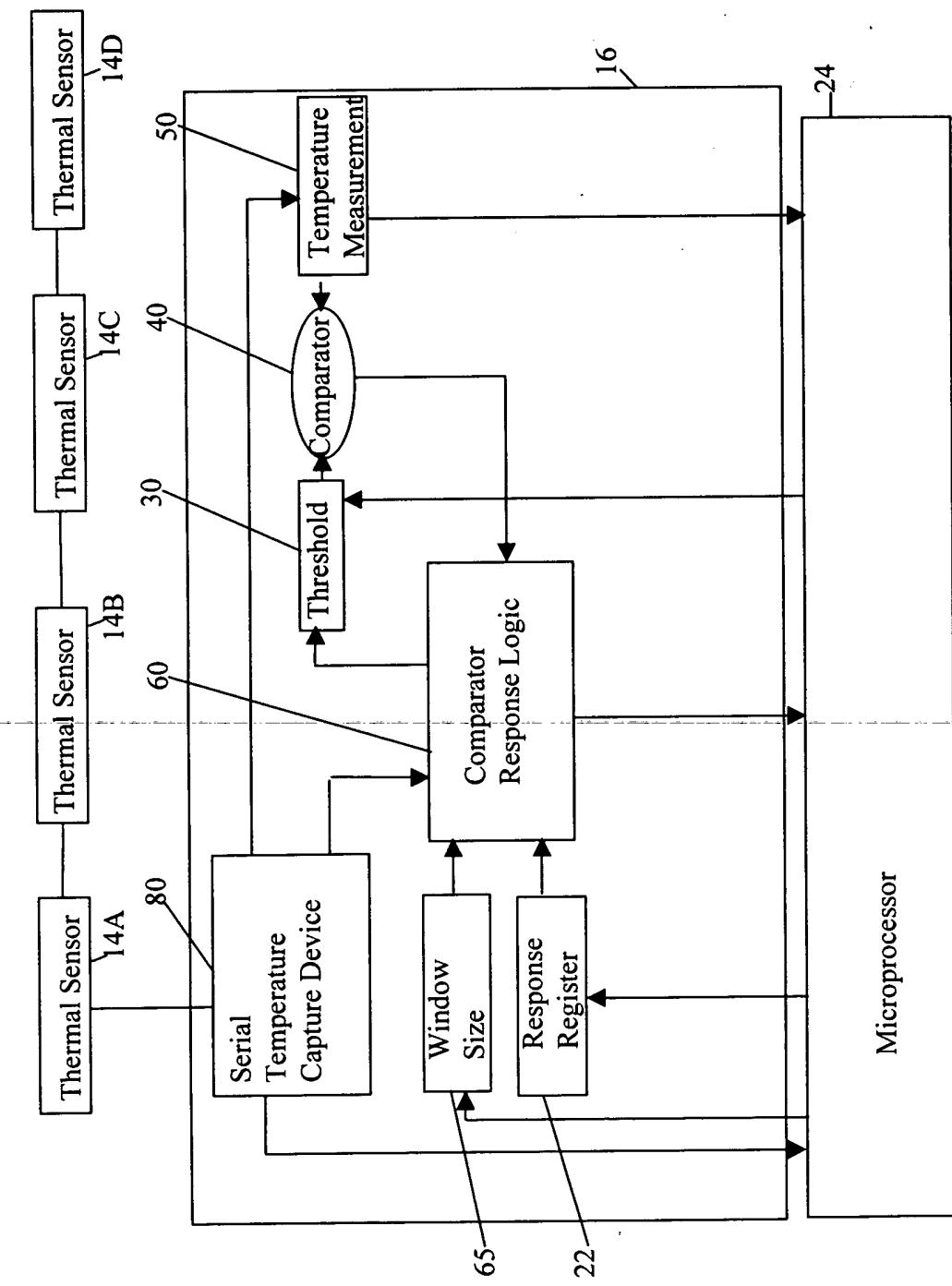


FIGURE 3

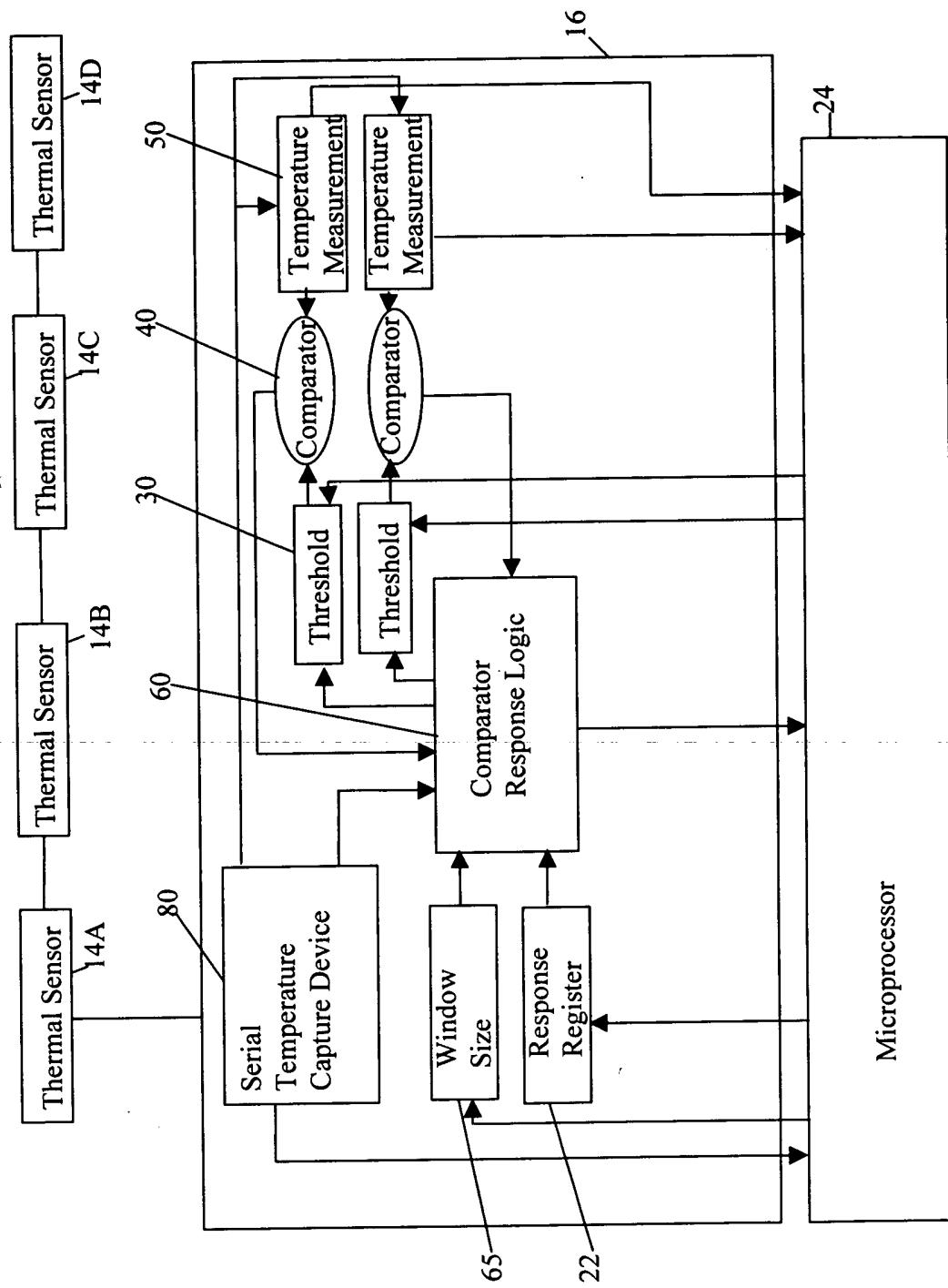


FIGURE 4

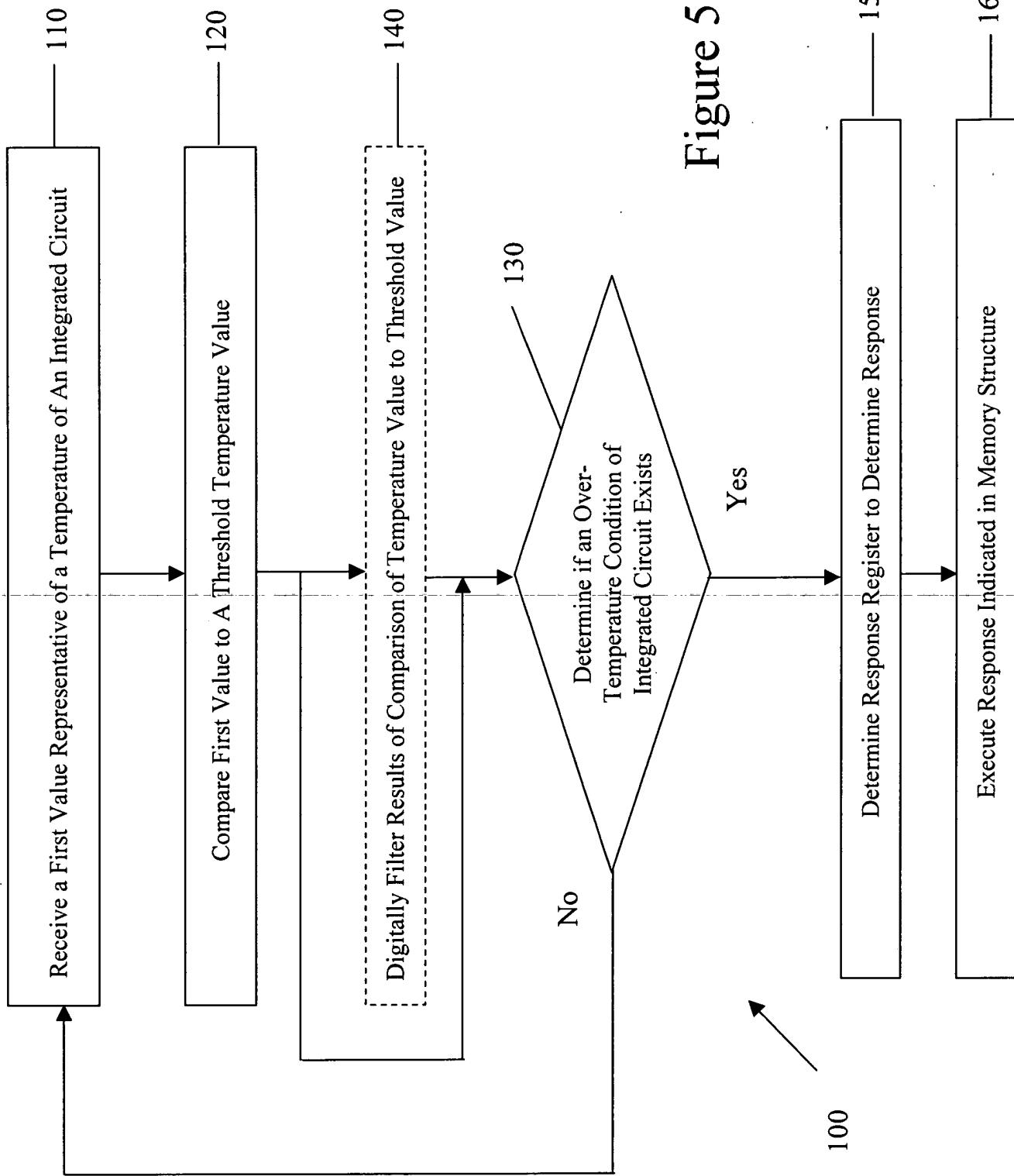


Figure 5